

The diagram illustrates the internal circuitry of a display device, organized into three main functional blocks:

- TEST CIRCUIT (701):** Located at the top, it consists of a series of test signal sources labeled S1, S2, S3, S4, S5, S6, S7, S8, S9, S10, S11, S12, Sn-1, and Sn. These sources are connected to a common horizontal line (501) and individual vertical lines (6a, 7a, 8a, 9a, 300, 3a).
- SCANNING LINE DRIVING CIRCUIT (104):** This block is responsible for driving the scanning lines. It includes a series of transistors (70, 7a, 8a, 9a, 300, 3a) and a series of gates (G1, G2, Gm) that control the flow of current through the test circuit components.
- DATA LINE DRIVING CIRCUIT (101):** Located at the bottom, it includes a series of data lines (114) and a series of data line driving transistors (116, 118) that provide the data signals to the display pixels.

The diagram also shows a **FIXED POTENTIAL LINE** at the top left and a **TEST CIRCUIT** label at the top center. Various other components and connections are labeled with numbers and letters, such as 503, 501, 300, 3a, 202, 301, 115, and 114.

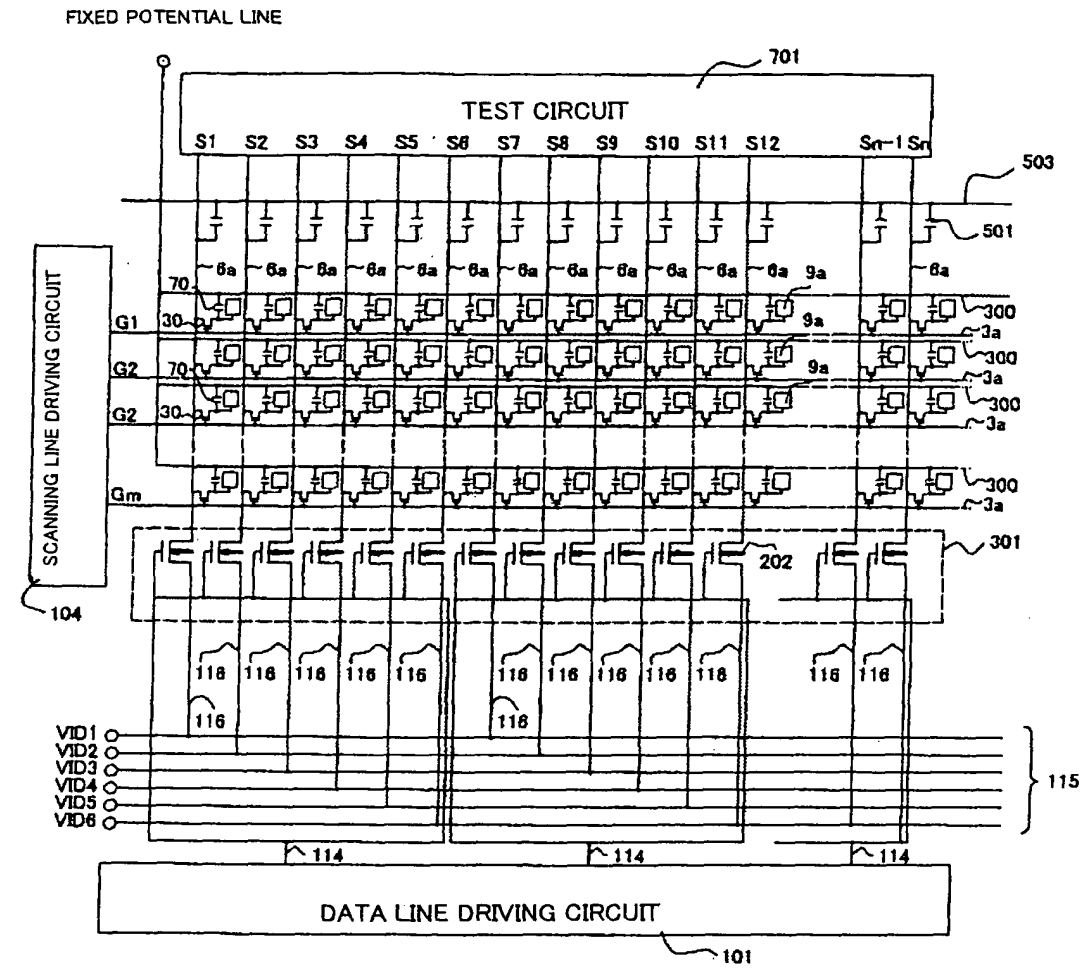




FIG. 3

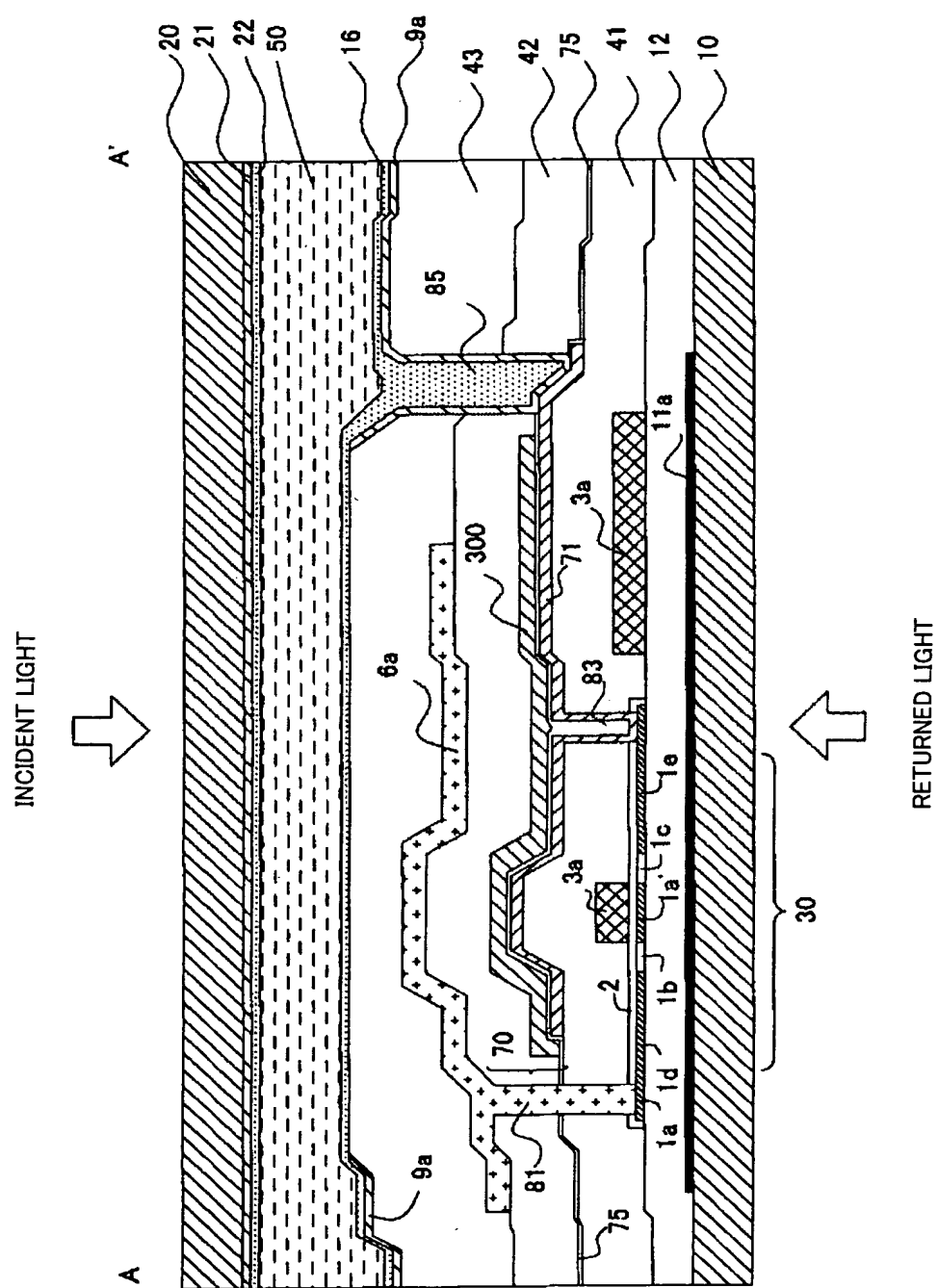


FIG. 4

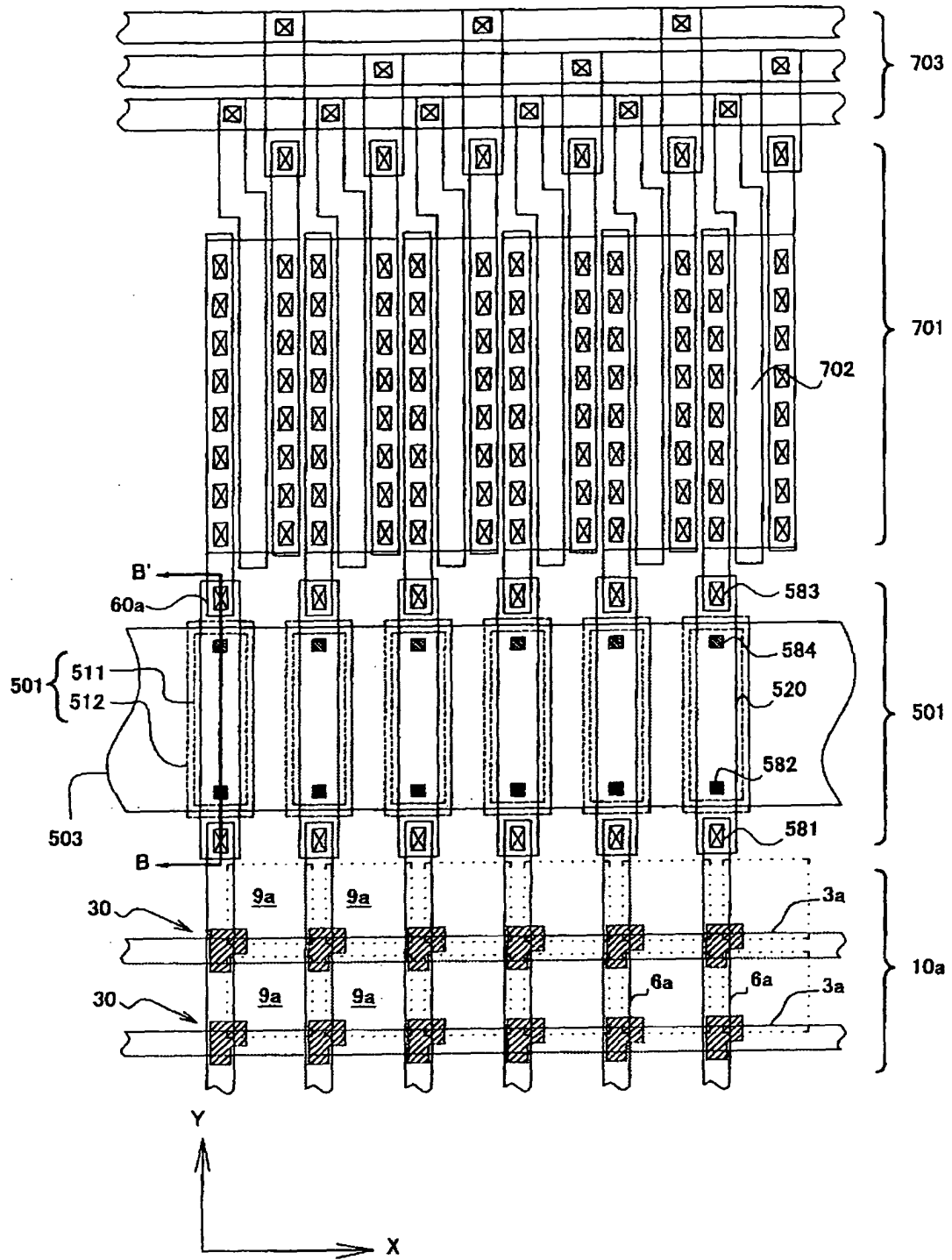




FIG. 6

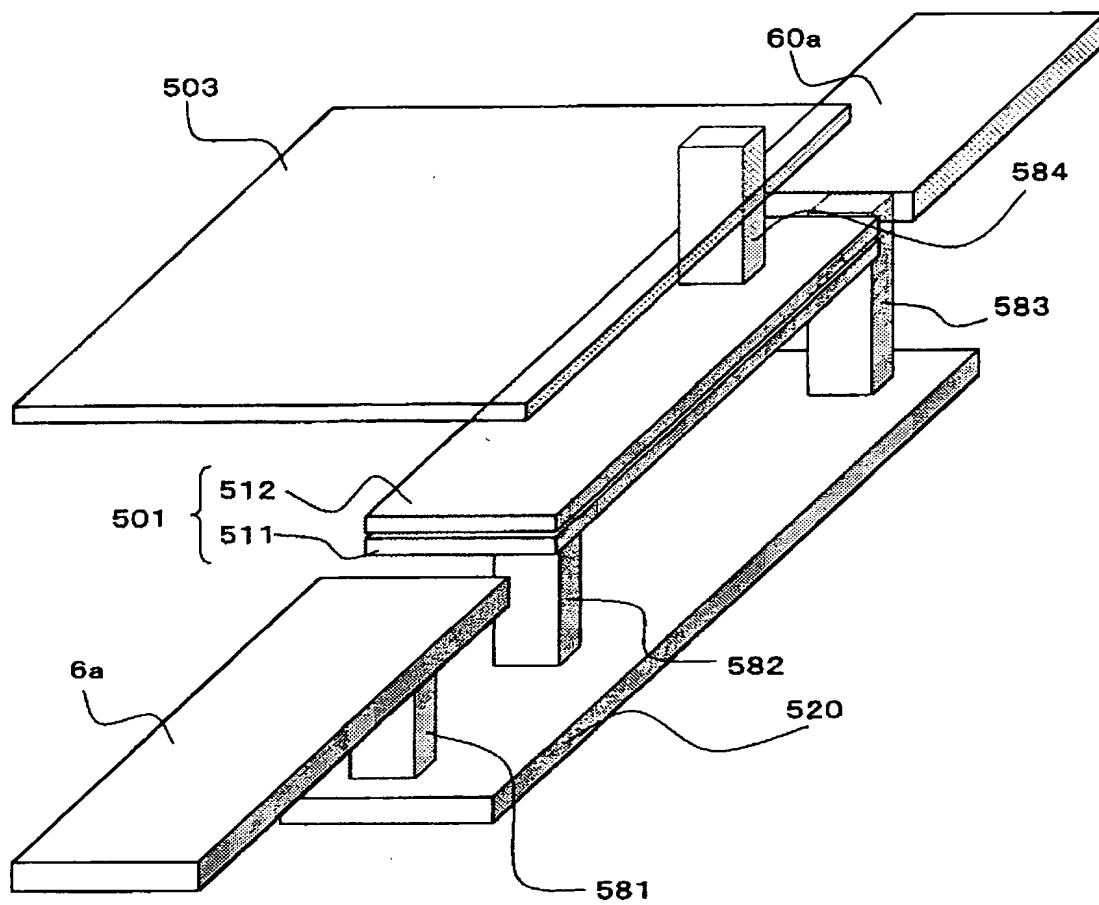


FIG. 7

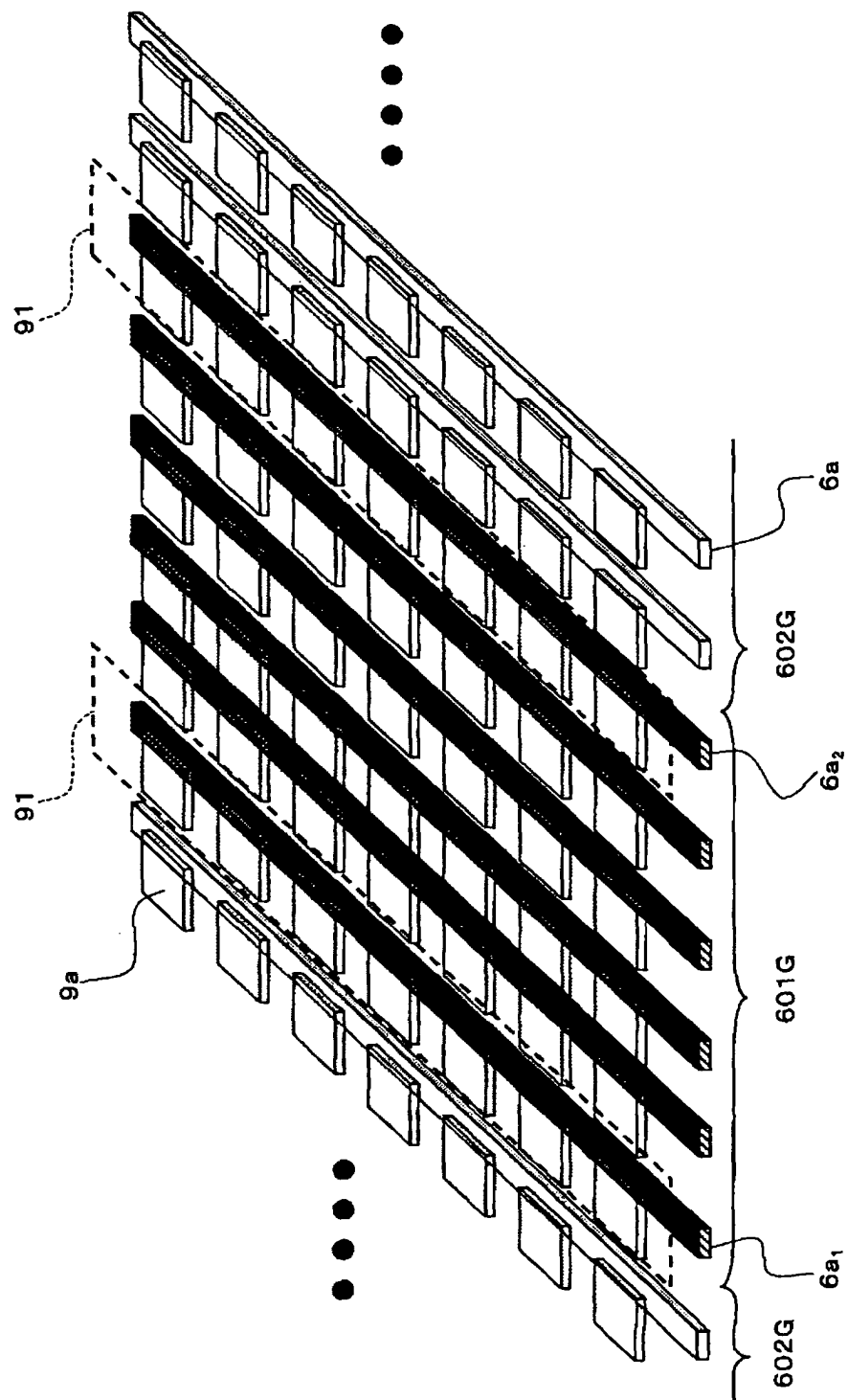


FIG. 8

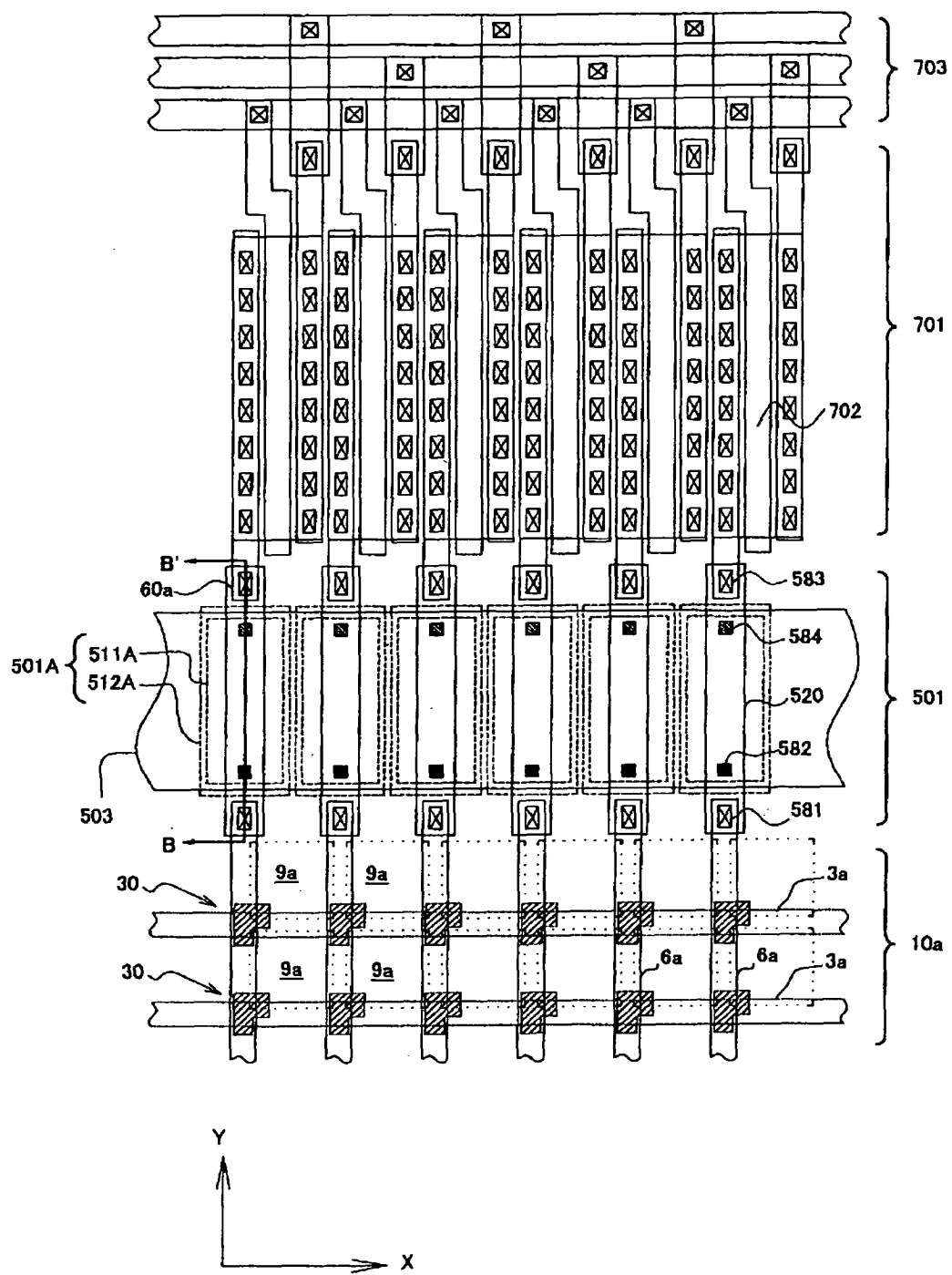




FIG. 9

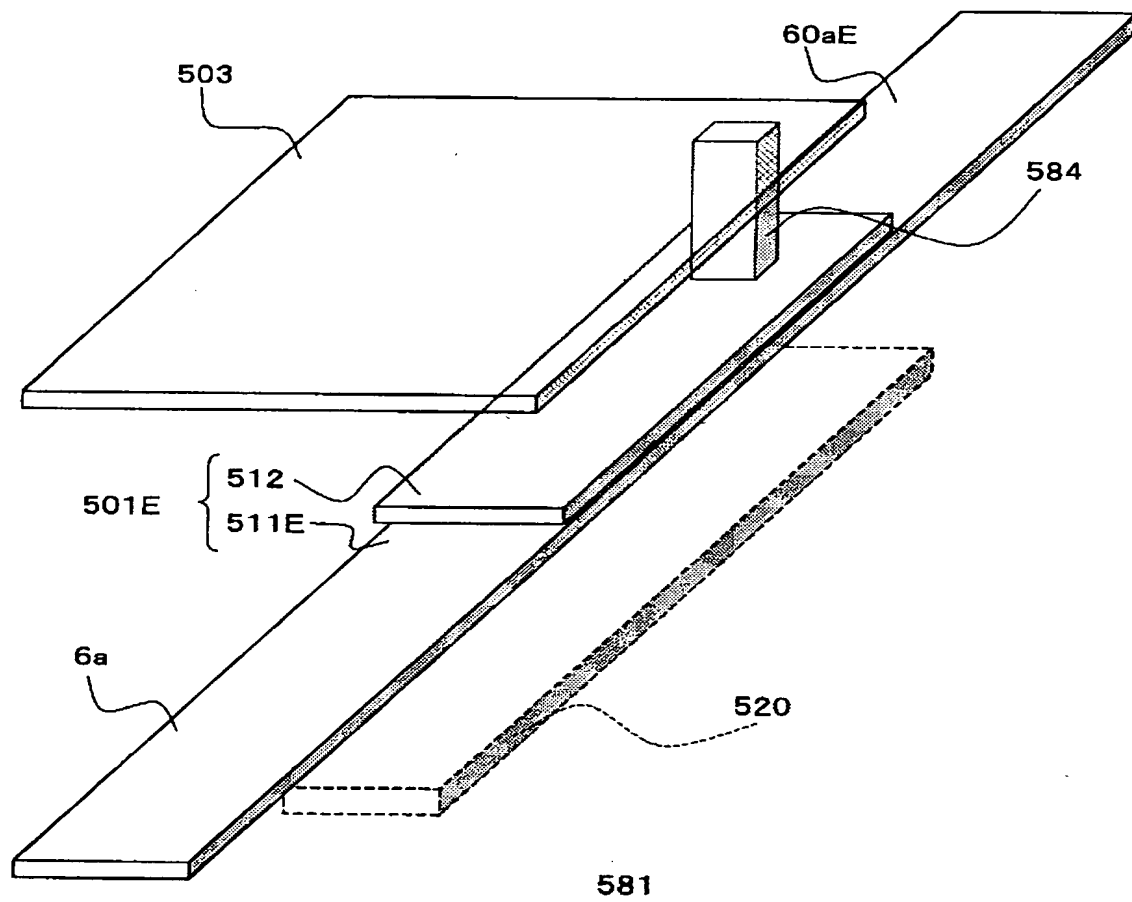
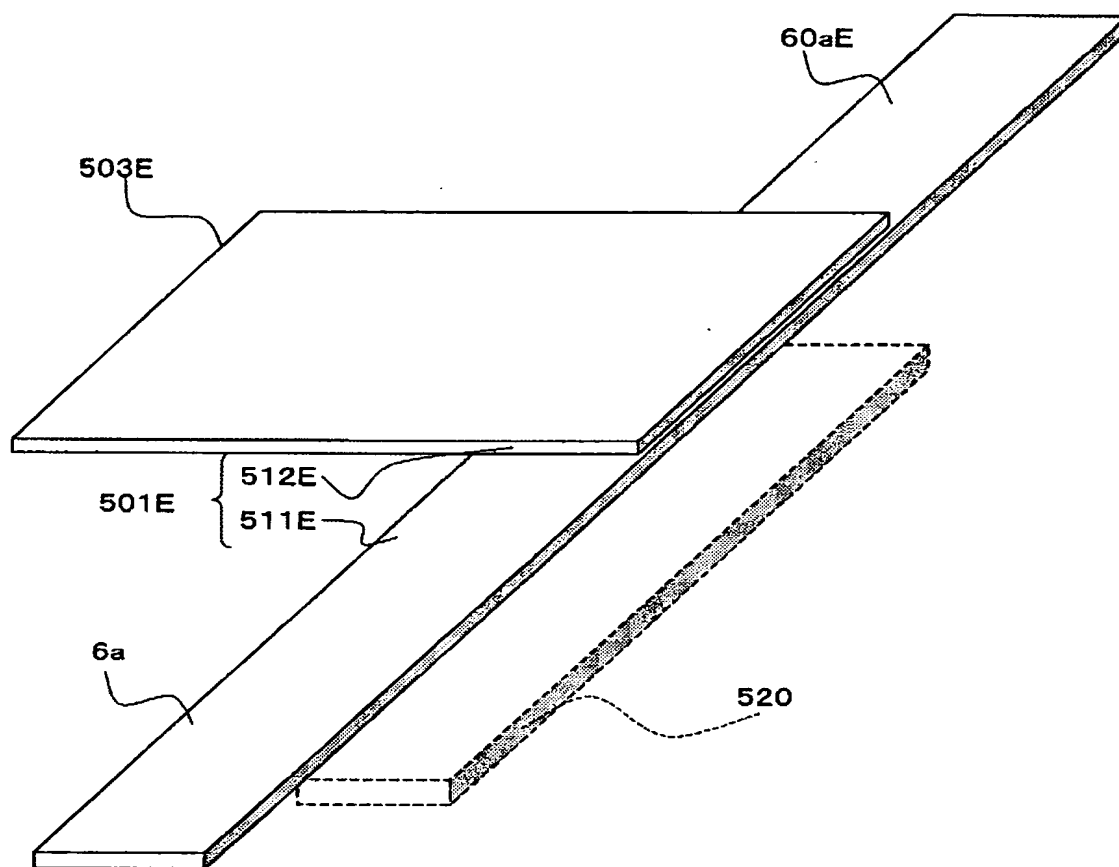
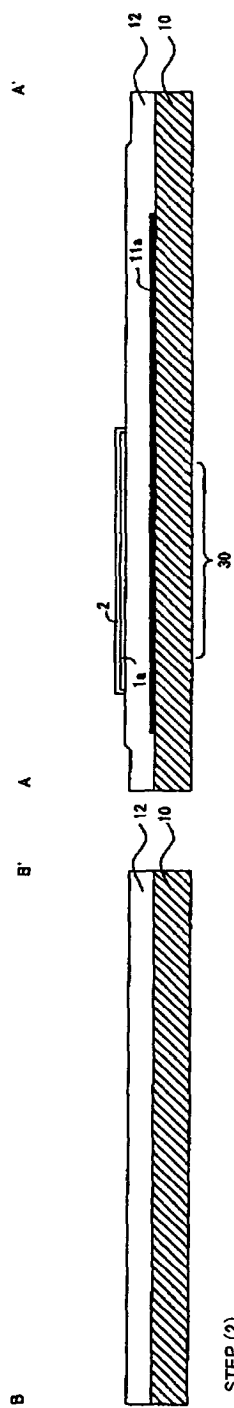


FIG. 10

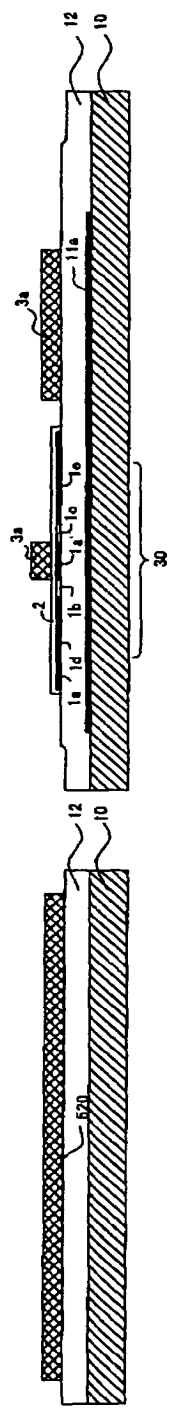


# FIG. 11

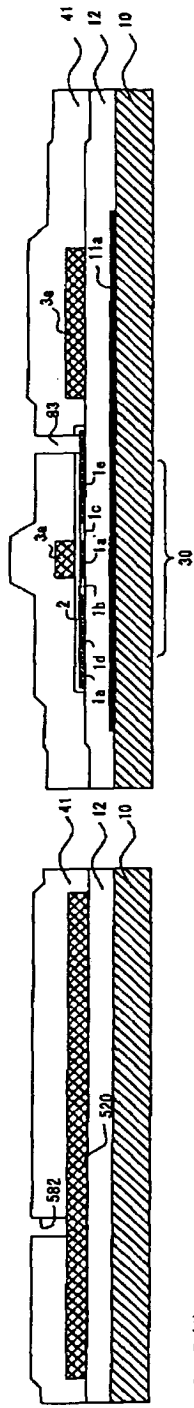
STEP (1)



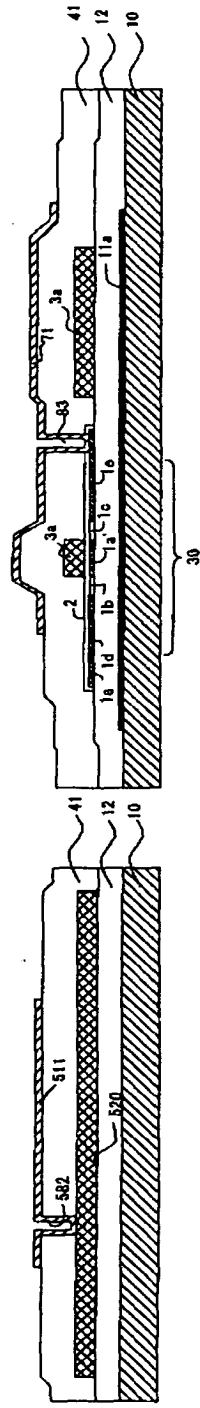
STEP (2)



STEP (3)



STEP (4)



# FIG. 12

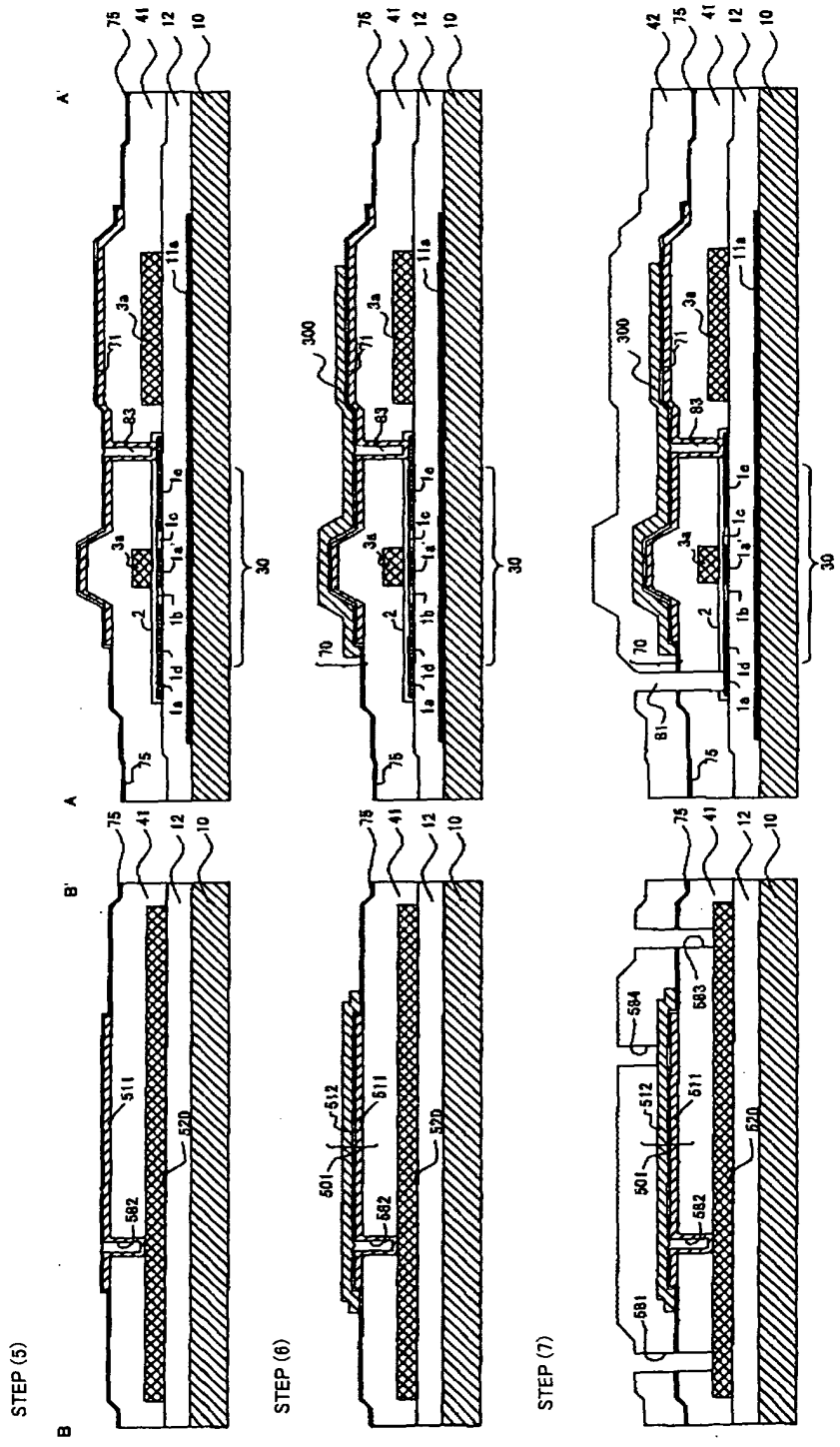
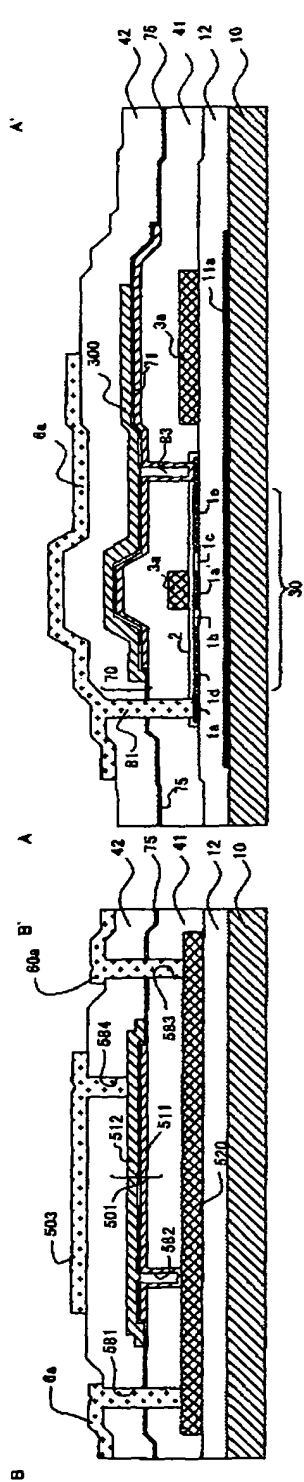


FIG. 13

STEP (8)



STEP (9)

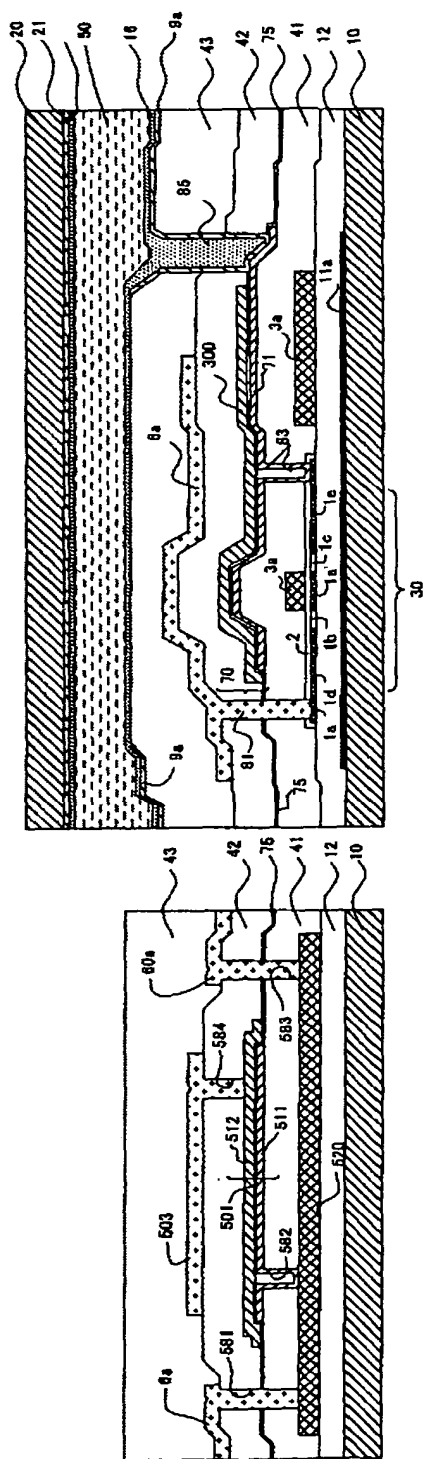


FIG. 14

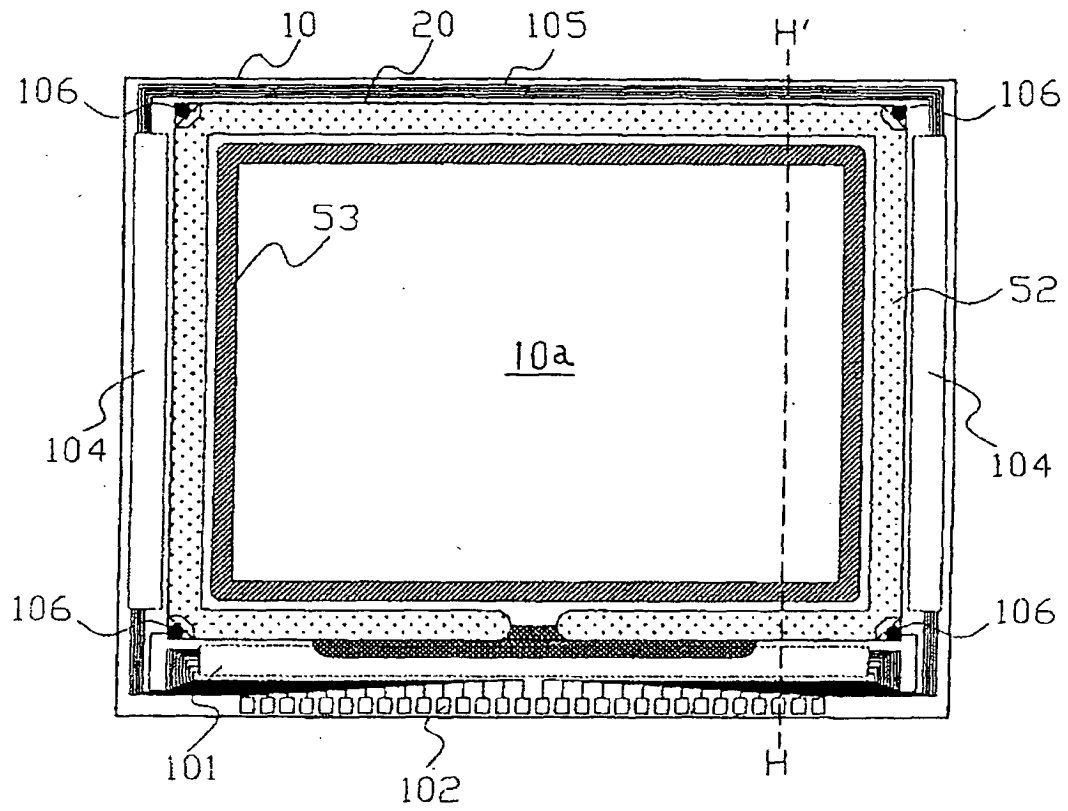


FIG. 15

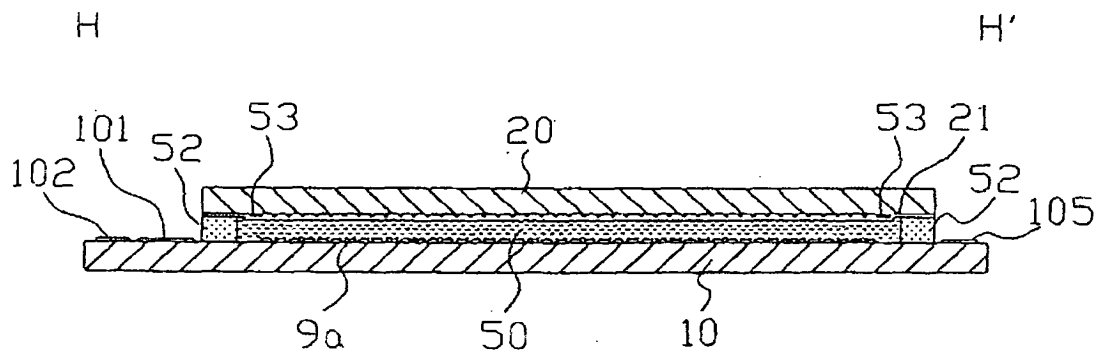


FIG. 16

